

Rockwell PLC5

Website: <http://www.ab.com>

Note: Rockwell PLC5 driver uses CRC checksum.

HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	Rockwell PLC5		
PLC I/F	RS232		
Baud rate	19200	9600, 19200	
Data bits	8	8	
Parity	Even	Even, Odd, None	
Stop bits	1	1	
HMI sta. no.	0		
PLC sta. no.	1	1-31	

PLC Setting:

Communication mode	DF1 Full Duplex protocol 19200, None, 8, 1 (default)
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Allen-Bradley PLC-5 Family PLCs use DF1 Full Duplex protocol.

For PLC-5/10, PLC-5/15 and PLC-5/25, MT8000 should be connected to the DF1 port on the 1785-KE module.

For PLC-5/11, PLC-5/20, PLC-5/30 and PLC-5/40, MT8000 should be connected to the Channel 0 Port on the PLC.

Device Address:

Bit/Word	Device	Format	Range	Memo
B	I1	DDDdd	0 ~ 25515	Input (I)
B	O0	DDDdd	0 ~ 25515	Output (O)
B	B3	DDDdd	0 ~ 99915	Bit data file (B3)
B	B10 ~ 13	DDDdd	0 ~ 99915	Bit data file (B10 ~ 13)
B	S_Bit	DDDDDDdd	0 ~ 25599915	
B	Bfn	FFFDDDDdd	0 ~ 25599915	
B	NfnBit	FFFDDDDdd	0 ~ 25599915	
W	T4SV	DDD	0 ~ 999	Timer Preset Value (T4)
W	T4PV	DDD	0 ~ 999	Timer Accumulator Value (T4)

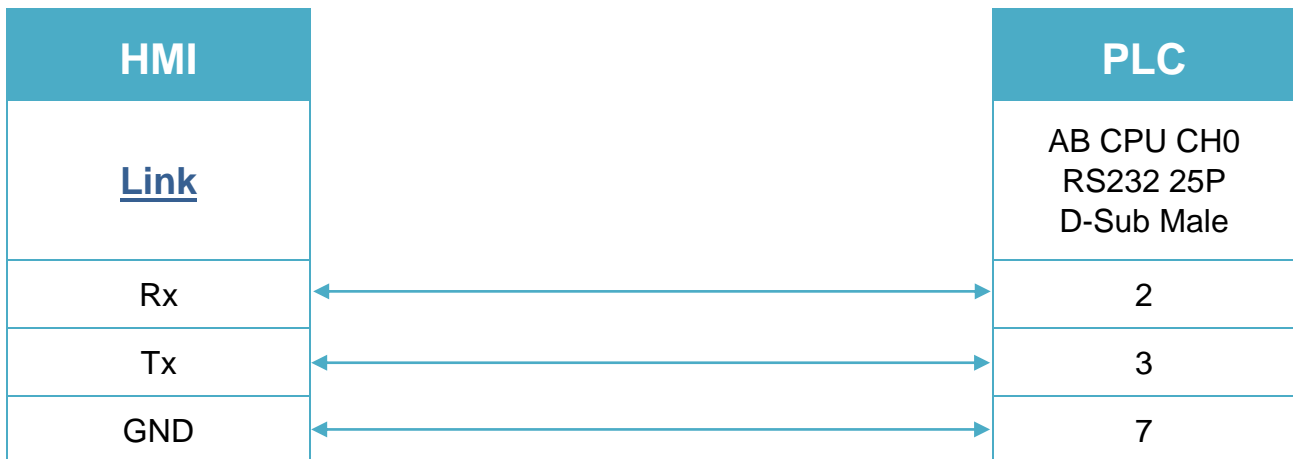
Bit/Word	Device	Format	Range	Memo
W	C5SV	DDD	0 ~ 999	Counter Preset Value (C5)
W	C5PV	DDD	0 ~ 999	Counter Accumulator Value (C5)
W	TfnSV	FFFDDD	0 ~ 255999	
W	TfnPV	FFFDDD	0 ~ 255999	
W	CfnSV	FFFDDD	0 ~ 255999	
W	CfnPV	FFFDDD	0 ~ 255999	
W	N7	DDD	0 ~ 999	Integer data file (N7)
W	N10 ~ 15	DDD	0 ~ 999	Integer data file (N10 ~ 15)
W	Nfn	FFFDDD	0 ~ 255999	Integer data file (V2.5.0 or newer)
W	S	DDD	0 ~ 255	
W	F8	DDD	0 ~ 999	Floating point data file (F8)
W	Ffn	FFFDDD	0 ~ 255999	Floating point data file (V2.5.0 or newer)

Wiring Diagram:

Diagram 1

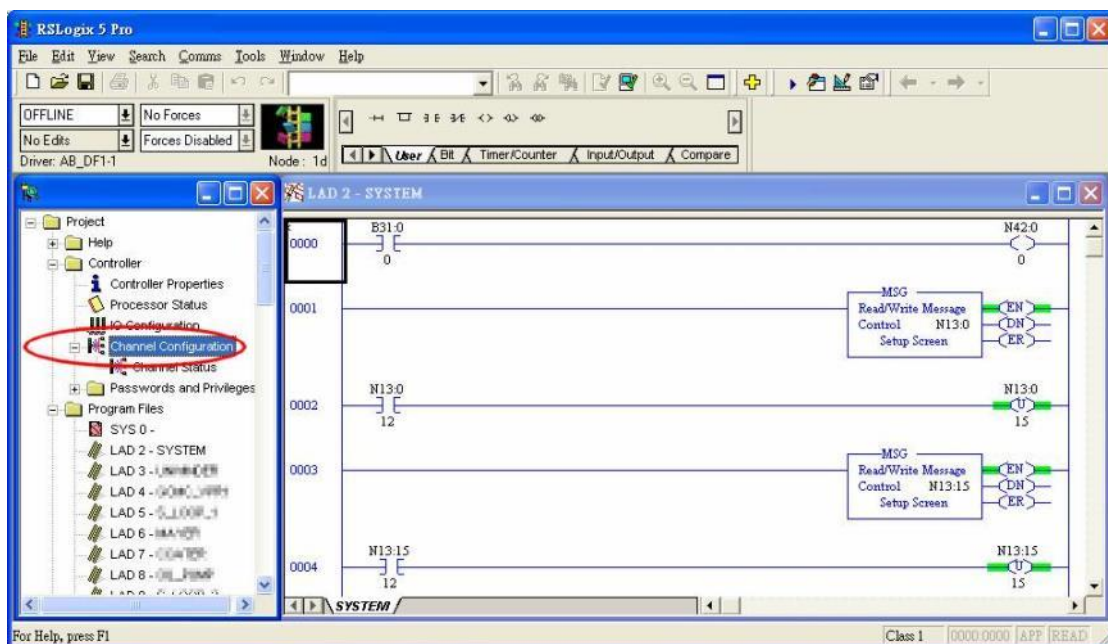
RS-232 (9P D-Sub to 25P D-Sub: PLC5 CPU CH0)

The serial port pin assignments may vary between HMI models, please click the following link for more information.



Note:

The default error checking of Rockwell PLC5 is BCC, whereas our driver is CRC.



Access [Channel Configuration] from RSLogix5, under Channel 0 tab, please select “CRC” for [Error Detect].

